

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/561,794
Priority Filing Date May 1, 2000
Inventor Alan R. Reinberg
Assignee Micron Technology, Inc.
Priority Group Art Unit 2811
Priority Examiner C. Nguyen
Attorney's Docket No. MI22-1704
Title: Microelectronic Device Fabricating Method, Integrated Circuit, and
Intermediate Construction

PRELIMINARY AMENDMENT

To: Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)
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Spokane, WA 99201-3828

AMENDMENTS

In the Specification

Please insert the following paragraph beginning at line 3 of page 1 with
the in accordance with 37 C.F.R. § 1.121(b)(1)(ii):

--RELATED PATENT DATA

This patent resulted from a divisional application of U.S. Patent
Application Serial No. 09/561,794 filed on May 1, 2000 which is a divisional of
U.S. Patent Application Serial No. 09/444,280 filed on November 19, 1999--.

In the Claims

Cancel claims 1-63 without prejudice

REMARKS

Claims 1-63 are canceled. Claims 64-67 are pending in the application.
Examination of claims 64-67 is requested.

Respectfully submitted,

Dated: 08 May 2001

By: 
James E. Lake
Reg. No. 44,854

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Assistant Commissioner for Patents
Washington, D. C. 20231
Attention: Official Draftsman

LETTER SUBMITTING FORMAL DRAWINGS

Please enter the enclosed formal drawings in the above-referenced application in place of the originally filed drawings.

Acknowledgment of receipt of the formal drawing sheets and their acceptance into the file is requested.

Respectfully submitted,

Date: 08 May 2001

By:


James E. Lake
Reg. No.: 44,854
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GREGORY & MATKIN P.S.
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Enclosures: Nine (9) sheets of drawings (Fig. 1A-20B)

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